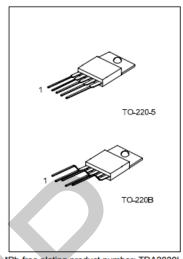


DESCRIPTION

The TDA2003 is a monolithic audio power amplifier integrated circuit.

FEATURES

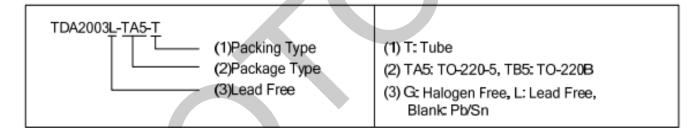
- Very low number of external component required.
- High current output (up to 3 A).
- Low harmonic and crossover distortion.
- Built-in Over temperature protection.
- Short circuit protection between all pins.
- Safety Operating Area for output transistors



*Pb-free plating product number: TDA2030L

ORDERING INFORMATION

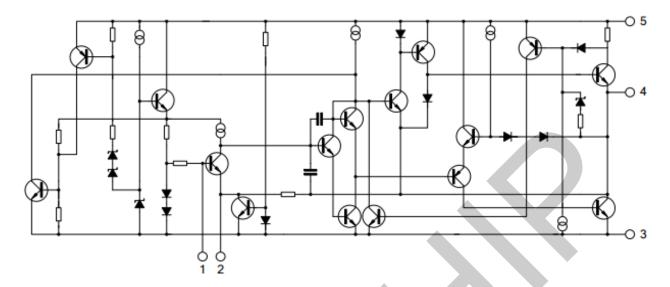
Orderin	g Number	Package	Dooking	
Normal	Normal Lead Free Plating		Packing	
TDA2003-TA5-T	TDA2003L-TA5-T	TO-220-5	Tube	
TDA2003-TB5-T	TDA2003L-TB5-T	TO-220B	Tube	



PIN CONFIGURATION

PIN NO.	PIN NAME				
1	Non inverting input				
2	Inverting inpu				
3	Ground				
4	Output				
5	Supply Voltage				

BLOCK DIAGR



ABSOLUTE MAXIMUM RATINGS

PARAMETER		SYMBOL	RATINGS	UNIT	
Supply Voltage		Vss	40	V	
DC Supply Voltage		Vss	28	V	
Operating Supply Voltage		Vss	18	V	
Output Pook Current	Repetitive	-	3.5	Α	
Output Peak Current	Non Repetitive	I _{O(PEAK)}	4.5		
Power Dissipation at Tc = 90 °C		P _D	20	W	
Storage and Junction Temperature		T _{STG}	-40 ~ +150	$^{\circ}$	

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

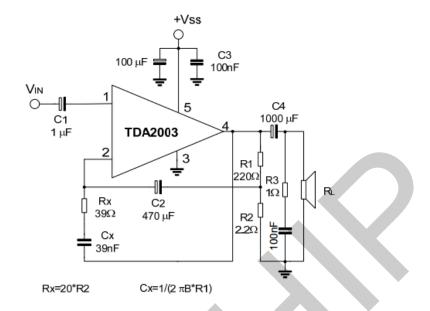


ELECTRICAL CHARACTERISTICS (Refer to the test circuit, Vs =±16V,Ta=25°C)

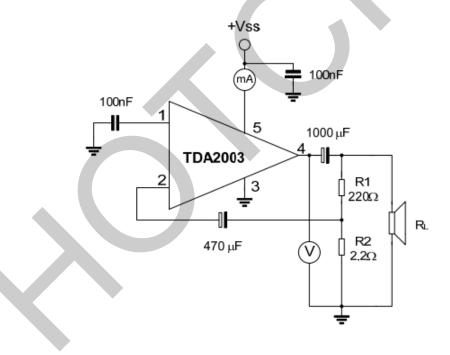
PARAMETER	SYMBOL	TEST CONDITIONS			MIN	TYP	MAX	UNIT	
		DC CH	ARA	CTERISTICS					
Supply Voltage	Vss				8		18	V	
Quiescent Output Voltage	Vouт				6.1	6.9	7.7	V	
Quiescent Drain Current	ID					44	50	mA	
		AC CHA	4RA	CTERISTICS		4			
				$R_L=4\Omega$	5.5	6			
Output Dower	POUT	THD=10)%,	R _L =2Ω	9	10		١٨/	
Output Power	POUT	f=1kH	Z	R _L =3.2Ω		7.5		- W	
				R _L =1.6Ω		1.2			
		f=1kHz		P_{OUT} =0.5W, R_L =4 Ω		14		mV	
land On a sitinity	.,,			P _{OUT} =6W, R _L =4Ω		55			
Input Sensitivity	Vı			P _{OUT} =0.5W, R _L =2Ω		10			
				P _{OUT} =10W, R _L =2Ω		50			
Input Saturation Voltage	V _{I(RMS)}				300			mV	
Frequency	F	Pout=1V	V, RL	_=4Ω	40		15000	Hz	
T () ()	THD	f=1kHz		P _{OUT} =0.05 ~ 4.5W,	0.15			0/	
Total Harmonic Distortion				P _{OUT} =0.05 ~ 7.5W,		0.15		%	
Input Resistance(Pin 1)	RI		open	loop, f=1kHz	70	150		kΩ	
Input Noise Current	RI					60	200	pА	
Input Noise Voltage	eN					1	5	μV	
0 1 1/1		7		f=1kHz		80		dB	
Open Loop Voltage Gain	Gvo	f=10kHz			60		dB		
Closed Loop Voltage	Gvc	f=1kHz, RL=4Ω		39.3	40	40.3	dB		
E.C		P _{OUT} =6W, RL=4Ω			69		0.1		
Efficiency, f=1kHz	η	P _{OUT} =10W, RL=2Ω			65		 %		
Supply Voltage Rejection	SVR	f=100Hz, V _{RUPPLE} =0.5V RG=10kΩ,			30	36		dB	



TEST CIRCUIT AC Test Circuit



DC Test Circuit

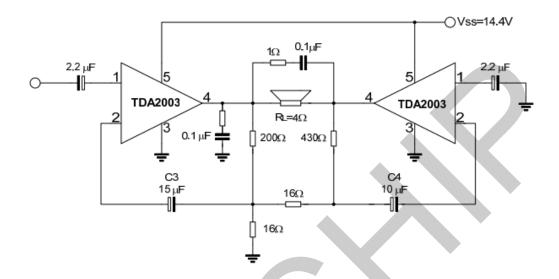




TYPICAL APPLICATION CIRCUIT

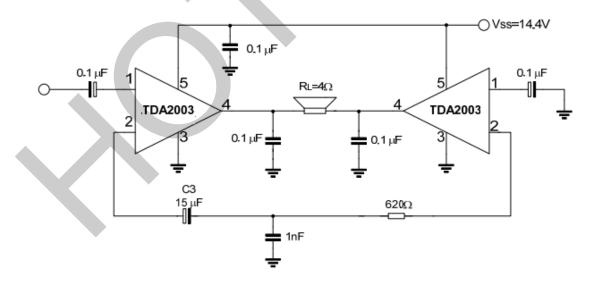
20W Bridge Configuration Application

The Values of the capacitors C3 and C4 are different to optimize the SVR (Typ. 40dB)



TYPICAL APPLICATION CIRCUIT

Low Cost Bridge Configuration Application Circuit(POUT=18W)



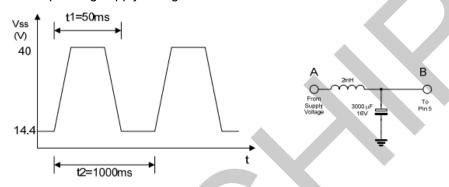


TDA2003(

BUILT-IN PROTECTION SYSTEMS LOAD DUMP VOLTAGE SURGE

The TDA2003 has a circuit which enables it to withstand a voltage pulse train, on pin 5.If the supply voltage peaks to more than 40V, then an LC filter must be inserted between the supply and pin 5, in order to assure that the pulses at pin 5 will be head within the limits.

A recommended LC network. With this network, a train of pulses with amplitude up to 120V and width of 2ms can be applied at point A. This type of protection is ON when the supply voltage(pulsed or DC) exceeds 18V. For this reason the maximum operating supply voltage is 18V.



SHORT CIRCUIT (AC and DC Conditions)

The TDA2003 can withstand a permanent short-circuit on the output for a supply voltage up to 16V.

POLARITY INVERSION

High current (up to 5A) can be handled by the device with no damage for a longer period than the blow-out time of a quick 1A fuse(normally connected in series with the supply).

The feature is added to avoid destruction if, during fitting to the car, a mistake on connection of the supply is made.

OPEN GROUND

When the radio is in the ON condition and the ground is accidentally opened, a standard audio amplifier will be damaged. On the TDA2003 protection diodes are included to avoid any damage.

INDUCTIVE LOAD

A protection diode is provide between pin 4 and pin 5(see the internal schematic diagram) to allow use of the TDA2003 with inductive loads. In particular, the TDA2003 can drive a coupling transformer for audio modulation.

DC VOLTAGE

The maximum operating DC voltage on the TDA2003 is 18V. However the device can withstand a DC voltage up to 28V with no damage. This could occur during winter if two batteries were series connected to crank the engine.



BUILT-IN PROTECTION SYSTEMS (Cont.) THERMAL SHUT-DOWN

The presence of a thermal limiting circuit offers the following advantages:

An overload on the output (even if it is permanent), or an excessive ambient temperature can be easily withstood.

The heat-sink can have a smaller factor compared with that of a conventional circuit. There is no device damage in case of excessive junction temperature: all that happens is that Po (and therefore PD) and Id are reduced

COMPONENTS USAGE SUGGESTION

The recommended values of the components are those shown on typical application circuit Different values

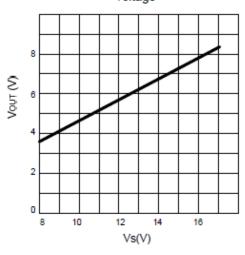
can be used. The following table can belo the designer.

can be used. The following table can help the designer.						
COMPONEN	RECOMMEND	PURPOSE	LARGE THAN RECOMMENDED	SMALLER THAN RECOMMENDED		
Т	ED VALUE		VALUE	VALUE		
R1	(Gv-1)*R2	Gain setting.		increase of drain		
131	(01 1) 112	Cum coung.		current		
R2	2.2Ω	gain and SVR setting	Decrease of SVR			
			Danger of oscillation at			
R3	1Ω	Frequency stability	high frequencies with			
			inductive loads.			
Rx	≈20R2	Upper frequency cutoff	Poor high frequencies attenuation	Danger of oscillation		
C1	2.2µF	Input DC decoupling		Noise at switch-on switch-of		
C2	470µF	Ripple rejection		Decrease of SVR		
C3	0.1µF	Supply voltage bypass		Danger of oscillation		
C4	1000µF	Supply voltage bypass		Higher low frequency		
04	Τοσομί	Supply voltage bypass		cutoff		
				Danger of oscillation at		
C5	0.1μF	Frequency stability		high frequencies with		
				inductive loads.		
Сх	≈1/(2π*B*R1)	Upper frequency cut of	Lower bandwidth	Larger bandwidth		

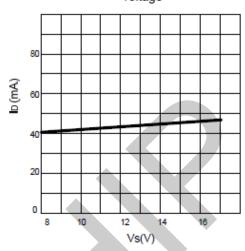


TYPICAL CHARACTORISTICS

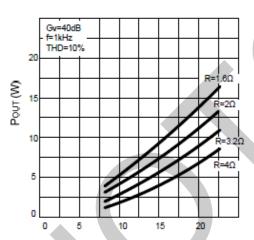
Quiescent output voltage vs.Supply voltage



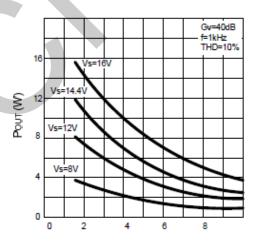
Quiescent drain current vs.Supply voltage



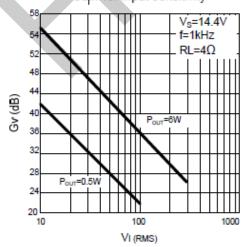
Output power vs. Supply voltage

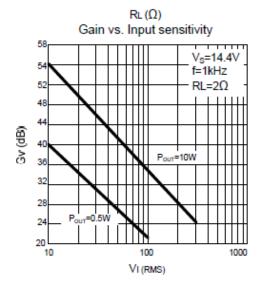


Output power vs.load resistance



Vs(V)
Gain vs. Input sensitivity

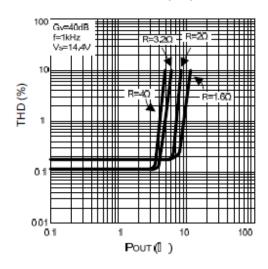




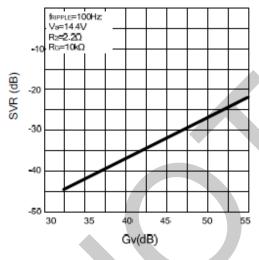


TYPICAL CHARACTORISTICS (Cont.)

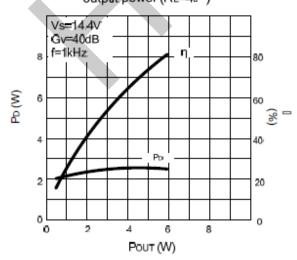
Distortion vs. output power



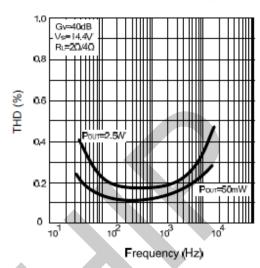
Supply voltage rejection vs. voltage gain



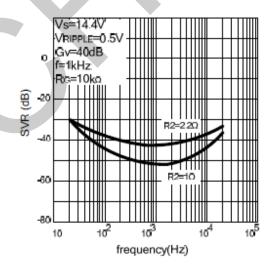
Power dissipation and efficiency vs. output power (RL=41)



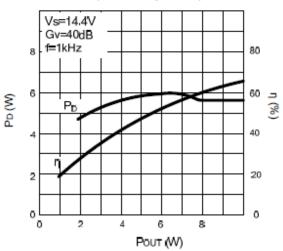
Distortion vs.frequency



Supply voltage rejection vs.frequency



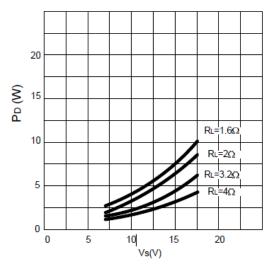
Power dissipation and efficiency vs. output power(RL=21)



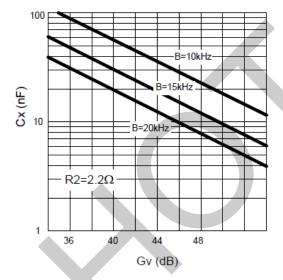


TYPICAL CHARACTORISTICS (Cont.)

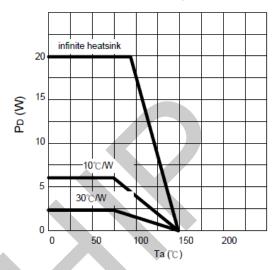
Maximum Power dissipation and supply voltage(sine wave operation)



Typical values of capacitor(Cx) for different values of frequency response



Maximum allowable dissipation and ambient temperature





PENTAWATT PACKAGE MECHANICAL DATA

DIM.		mm		inch			
DIW.	MIN	TYP	MAX	MIN	TYP	MAX	
Α			4.80			0.189	
С			1.37			0.054	
D	2.40		2.80	0.094		0.110	
D1	1.20		1.35	0.047		0.053	
E	0.35		0.55	0.014		0.022	
E1	0.76		1.19	0.030		0.047	
F	0.80		1.05	0.031		0.041	
F1	1.00		1.40	0.039		0.055	
G	3.20	3.40	3.60	0.126	0.134	0.142	
G1	6.60	6.80	7.00	0.260	0.268	0.276	
H2			10.40			0.409	
H3	10.05		10.40	0.396		0.409	
L	17.55	17.85	18.15	0.691	0.703	0.715	
L1	15.55	15.75	15.95	0.612	0.620	0.628	
L2	21.20	21.40	21.60	0.831	0.843	0.850	
L3	22.30	22.50	22.70	0.878	0.886	0.894	
L4			1.29			0.051	
L5	2.60		3.00	0.102		0.118	
L6	15.10		15.80	0.594		0.622	
L7	6.00		6.60	0.236		0.260	
L9		0.2			0.008		
М	4.23	4.50	4.75	0.167	0.177	0.187	
M1	3.75	4.00	4.25	0.148	0.157	0.167	
V4			40° (t	yp.)	<u> </u>		
Dia	3.65		3.85	0.144		0.152	

